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3.2 COMPONENT DESCRIPTIONS

3.2.1 INTEGRATED CIRCUIT

➤ P89V51RD2

The P89V51RD2 is an 8051 microcontroller with 64 kB Flash and 1024 bytes of data RAM.

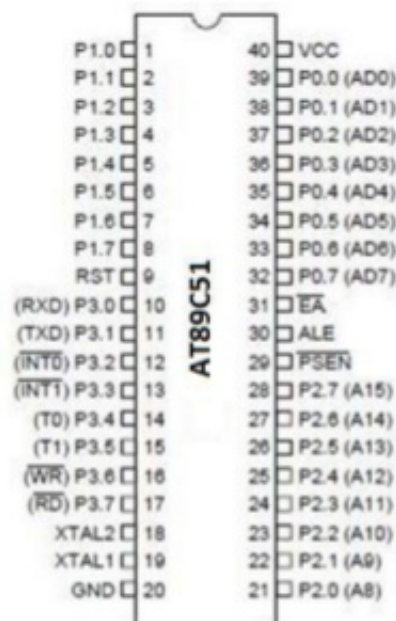


Fig 3.1: P89V51RD2 Pin Diagram

P0.0 to P0.7 -Port 0: Port 0 is an 8-bit open drain bidirectional I/O port. Port 0 pins that have '1's written to them float, and in this state can be used as high-impedance inputs. Port 0 is also the multiplexed low-order address and data bus during accesses to external code and data memory. In this application, it uses strong internal pull-ups when transitioning to '1's. Port 0 also receives the code bytes during the external host mode programming, and outputs the code bytes during the external host mode verification. External pull-ups are required during program verification or as a general purpose I/O port.

P1.0 to P1.7 -Port 1: Port 1 is an 8-bit bidirectional I/O port with internal pull-ups. The Port 1 pins are pulled high by the internal pull-ups when '1's are written to them and can be used as inputs in this state. As inputs, Port 1 pins that are externally pulled LOW will

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